



MS AF
REPLY UNDER
37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2826

PATENT
0397-439P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: H. MATSUMOTO et al. Conf.: 5766
Appl. No.: 10/054,823 Group: 2826
Filed: January 25, 2002 Examiner: P. Greene
For: INTEGRATED SEMICONDUCTOR CIRCUIT DEVICE
HAVING SCHOTTKY BARRIER DIODE (as amended)

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REPLY AFTER FINAL UNDER 37 C.F.R. § 1.116

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 21, 2003

Sir:

In reply to the Office Action dated May 21, 2003, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

This reply includes:

Amendments to the Drawings begin on page 2 of this paper and include an attached replacement sheet.

Amendments to the Claims are reflected in the listing of claims, which begins on page 3 of this paper.

Remarks begin on page 5 of this paper.

Conclusion begins on page 8 of this paper.

AMENDMENTS TO THE DRAWINGS

Attached hereto are Figures 5 and 6 of corrected formal drawings that comply with the provisions of 37 C.F.R. § 1.84. The corrected formal drawings incorporate the following drawing changes:

Corrected formal drawings for Figures 5 and 6, having a label "Prior Art" are provided herewith.

It is respectfully requested that the corrected formal drawings be approved and made a part of the record of the above-identified application.